

Appendix1 TUT Requirements of Courses

Name	ECTS	Place of Completion	Categories
Introduction to Entrepreneurship	<u>4</u>	<u>TUT</u>	GENERAL STUDIES (compulsory) At least 10 ECTS
Entrepreneurship and Business Planning	<u>6</u>	<u>TUT</u>	
Giving Presentations in English	<u>3</u>	<u>TUT</u>	GENERAL STUDIES (Optional) At least 6 ECTS
Innovation and creative problem solving	<u>3</u>	<u>TUT</u>	
Technology Enterprise Creation and Financing	<u>3</u>	<u>TUT</u>	
Student's Enterprise Practice	<u>3</u>	<u>TUT</u>	
Computer Systems Engineering	<u>5</u>	<u>TUT</u>	CORE STUDIES (compulsory) At least 10 ECTS
Microprocessor Systems	<u>5</u>	<u>TUT</u>	
Dependability and Fault Tolerance	<u>5</u>	<u>TUT</u>	CORE STUDIES (Optional) At least 10 ECTS, including 5 ECTS gained at USST
Foundations and Management of Cyber Security	<u>5</u>	<u>TUT</u>	
Data Acquisition Means and Methods	<u>5</u>	<u>TUT</u>	
<u>Applied Data Communication</u>	<u>5</u>	<u>USST</u>	
Design for Test	<u>5</u>	<u>TUT</u>	SPECIAL STUDIES (compulsory) At least 14 ECTS, including 10 ECTS gained at USST)
Digital Systems Modelling and Synthesis	<u>5</u>	<u>TUT</u>	
Practical Training	<u>4</u>	<u>TUT</u>	
<u>Advanced Course in Programming I</u>	<u>5</u>	<u>USST</u>	
Timing Analysis of Software Dynamic Properties	<u>5</u>	<u>TUT</u>	
Verification of Digital Systems	<u>5</u>	<u>TUT</u>	SPECIAL STUDIES (Optional) At least 20 ECTS (Including 15 ECTS gained at USST)
<u>Embedded Systems</u>	<u>5</u>	<u>USST</u>	
Systems-on-Chip Design	<u>5</u>	<u>TUT</u>	
Digital Systems Design	<u>5</u>	<u>TUT</u>	
<u>Software Project Management</u>	<u>5</u>	<u>USST</u>	
Basics of Computer Aided-Design	<u>5</u>	<u>TUT</u>	
Analysis of Programming Languages	<u>5</u>	<u>TUT</u>	
<u>Databases I</u>	<u>5</u>	<u>USST</u>	